

SHOP NOTES

These are "how to do it" papers. They should be written and illustrated so that the reader may easily follow whatever instruction or advice is given.

Electron-beam lithography of multiple-layer submicrometer periodic arrays on a barium fluoride substrate

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I. INTRODUCTION

Direct-write electron-beam fabrication of periodic arrays of submicrometer metallic structures onto Si wafers has been demonstrated¹⁻³ for use as infrared (IR) frequency selective surfaces (FSSs). Typically these fabrications have used Si for convenience and because of its broad spectral range of IR transparency from 2 to past 14 μm . However, the high refractive index (3.4) of Si is not ideal for some applications because it implies a high surface reflectance and also because of the resulting shrinkage of structure sizes to maintain a given electrical size when fabricated on the Si/air interface. The goal of this work was to explore the feasibility of using BaF_2 as a low-index substrate material in multiple-layer FSS designs. While BaF_2 has low index (1.4) and good transmission in the IR, it presents unique fabrication issues including difficulties of obtaining a return from a laser-based height monitor, susceptibility to thermal shock, and attack by etchants. We present procedures we used to successfully fabricate submicron metallic arrays on BaF_2 in both single-layer and multiple-layer configurations.

II. BARIUM FLUORIDE OPTICAL AND PHYSICAL PROPERTIES

The BaF_2 wafers were obtained from Global Optics (UK) with the following dimensions: 50.4 mm diameter ± 0.1 mm, thickness of 3.10 mm $+0.00/-0.05$ mm. The wafers were also specified to be optically flat ($\lambda/10$ at 633 nm) which more than satisfied optical flatness requirements at IR wavelengths. The optical properties of the wafers used were measured using an IR variable-angle spectroscopic ellipsometer. The measured results for the optical constants, n (refractive index) and k (extinction coefficient), are shown in Fig. 1. As measured, BaF_2 is sufficiently transparent from 2 to 12 μm to be appropriate for use in transmissive IR FSS structures.

The physical properties of BaF_2 were also of interest from the point of view of their impact on the lithographic process. BaF_2 is susceptible to thermal shock because of the low thermal conductivity, $7.1 \text{ W m}^{-1} \text{ C}^{-1}$, and the high thermal ex-

pansion coefficient, $18 \times 10^{-6} \text{ C}^{-1}$.⁴ As a comparison, Si has a thermal conductivity of $159 \text{ W m}^{-1} \text{ C}^{-1}$ and a thermal expansion coefficient of $2.55 \times 10^{-6} \text{ C}^{-1}$.⁵ Therefore care was taken to prevent thermal-stress fracturing of the wafer when heating and cooling the substrate during the lithographic processing. Another relevant physical property is that BaF_2 is soluble in both acids and bases.⁶

III. ELECTRON-BEAM LITHOGRAPHY ON BaF_2

The FSS fabricated onto the BaF_2 substrate had a minimum feature size of 500 nm. The e-beam lithography tool used was a Leica EBPG 5000+ which is capable of line widths as small as 20 nm.⁷ To write the structure with good edge definition, a spot size of approximately 100 nm was used with an approximately 100 nA beam current at an accelerating voltage of 50 kV. The resist used was ZEP520A-7 with a thickness of 300 nm.

To ensure good fidelity of the written pattern, it is important to create a route to ground for the incident electrons, which avoids charge-accumulation artifacts. Since BaF_2 is nonconductive, a conductive layer was required. In addition, the electron beam has to be precisely focused onto the surface being written to yield minimum beam-spot size and proper dosage calibration. The e-beam writer measures the local height to the surface using a visible laser at $\lambda = 633 \text{ nm}$ and a quad-cell detector. Thus, the surface to be written must be sufficiently reflective to allow good height measurements. Because of the high transparency and low refractive index of BaF_2 at visible wavelengths, there was not enough power reflected to allow measurement of the surface height.

To remedy the issues of both the electron charging and the reflection of the surface, two methods were used to enable writing onto the BaF_2 surface. The first remedy was to coat the entire resist surface with thin layer of nickel. The other technique was to use a spin-on conductive polymer,⁸ Aqua-Save, along with a ring of sputtered nickel outside the region to be written. Each of these techniques will be discussed for their merits and limitations for e-beam lithography on the BaF_2 substrate.

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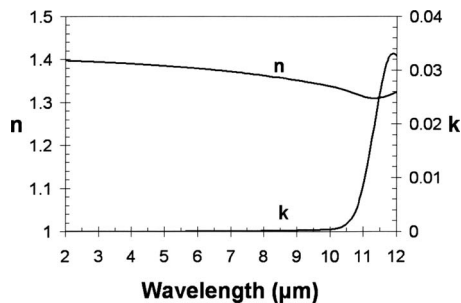


FIG. 1. Measured optical properties of the BaF_2 substrate in the range of 2–12 μm showing the refractive index n and extinction coefficient κ .

The first method involved sputtering a 40 nm thickness layer of Ni onto the resist, using a Materials Research Corporation 8667 sputtering system. The advantage of using a full overcoat of Ni is that one can write on the entire surface of the wafer. The difficulty with using the full coat of Ni directly on the resist is that Ni must be removed before development of the resist. We tried two methods to remove the Ni layer. The first method was to wet etch Ni using Transene TFA nickel etchant, a nitric acid solution. Unfortunately, the etchant was found to aggressively attack the BaF_2 wafer, ruining the surface quality within seconds. The second method was to remove Ni using 46 cm width adhesive tape (Controlled Environment Equipment Corp., polyfoam sticky roller), which is generally used to remove particles from cleanroom floors. Its width made it ideal for removal of the Ni layer from above the ZEP, since it was wider than the wafer's diameter. Ni adheres very poorly to ZEP resist compared to its adherence to the tape, and thus the metal is readily removed from the surface of the resist using the tape. However, it is important that the tape be used only once over the surface. When the tape is used only once, by making the size of the tape larger than the surface of the wafer, the tape adhesive never comes into contact with the resist layer. If used repeatedly, the adhesive will come into contact with the resist and one of two problems will occur. The first problem will be the potential removal of resist since the resist adheres to the tape more strongly than the resist adheres to the substrate. The second problem is that remnants of the adhesive will remain on the surface of the resist which will prevent the development of those regions. After Ni is removed, the surface is ready for development.

The second method was to spin on a conductive polymer, AquaSave, above the ZEP resist layer. The AquaSave was spun onto the wafer at 3000 rpm and then baked at 110 °C for 90 s. After baking, the hotplate was set to cool slowly at a rate of about 4 °C/min until room temperature was reached to prevent the BaF_2 wafer from cracking from thermal shock. Once the wafer returned to room temperature, a circular piece of Al foil was used as a crude mask. The foil was simply placed over the center of the wafer and 40 nm of Ni was sputtered onto the surface. This method allowed for a conductive discharge layer, the conductive polymer, over the entire wafer while having only an optically reflective Ni layer at the edge of the wafer. The e-beam writer used a

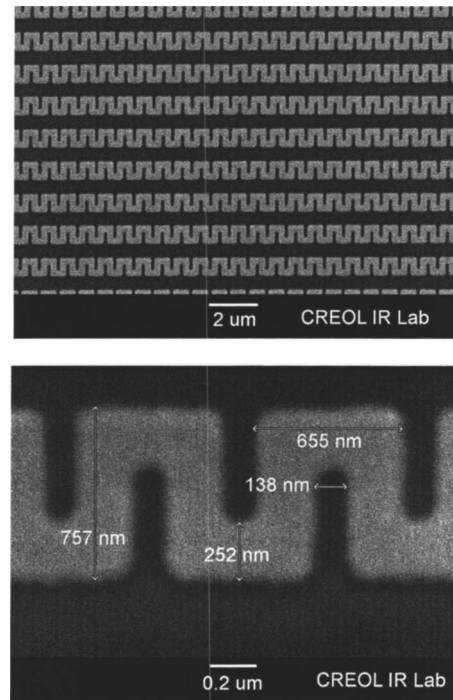


FIG. 2. Electron micrographs of the Al array structure on the BaF_2 substrate.

modified program to determine the wafer height. The modification was to measure heights at the edge of the wafer in the regions of sputtered Ni and to fit these points to a plane. This approach worked since the wafer was substantially flat. The planar fit function was then used to compute the height at each location on the wafer and to modify the beam focus accordingly. The advantage for this method is in multiple-layer lithography, since one can see through the AquaSave layer for alignment purposes. Therefore the conductive polymer was used for the second layer in the double-layer FSS design, but the whole-surface Ni coating was used for the first layer write, since this process required one less heating and cooling cycle.

The developer used was ZEP-RD and the development time was 30 s. With this development time, the dose used was 90 $\mu\text{C}/\text{cm}^2$. After development, the wafer was barrel etched in an O_2 plasma for 2 min at a rf power of 200 W using a Branson barrel etcher. This final etch was to remove any remaining resist in the developed regions.

Following the development and barrel etching, Al was deposited via a thermal evaporation to a thickness of 75 nm. Once metallized, the wafer was placed into a bath of methylene chloride (CH_2Cl_2) to remove the resist, leaving the desired structures on the BaF_2 surface. Images of the fabricated structures are shown in Fig. 2. Once the first FSS layer was written, a second FSS layer could be fabricated once a standoff layer was deposited.

IV. MULTIPLE-LAYER LITHOGRAPHY ON BAF_2

For fabrication of multiple-layer FSS structures, we used a dielectric standoff material that had desirable optical properties (low absorption in the IR band of interest and similar

refractive index to BaF₂), which could be deposited in various thicknesses and which had good adherence to the substrate and to the metallic FSS structures. We used a spin-on dielectric from Dow Chemical Company, CYCLOTENE 3022-35, a benzocyclobutene (BCB) polymer. The optical properties of the BCB layer were previously presented.³ Since BCB can be spun onto a wafer, the thickness can be controlled by the spin speed. Also, the capability to spin on the dielectric layer as a liquid also allows for a much smoother top-surface topography; this planarization is very helpful for subsequent e-beam lithographic steps.⁹ Neither sputtering nor evaporation was suitable for deposition of the standoff layer because these processes produce conformal films, with the top surface of the deposited dielectric having the same topography as the underlying surface.

The process for deposition of a BCB layer with a thickness of 1.3 μm began with an application of adhesion promoter (Dow AP3000), which was spun on at 2000 rpm for 20 s. The BCB was then applied with a spin speed of 3000 rpm to achieve the desired thickness, using the viscosity of the undiluted product. Following the spin, BCB requires two heating steps to cure the polymer. The first step is to drive off the solvent (mesitylene) using a low-temperature bake at 180 °C for 5 min on a hotplate. Without removing the wafer, the temperature was then increased to 250 °C to cure the BCB. For this step the substrate was covered with a glass dish, incorporating a low-pressure N₂ gas flow to prevent ambient O₂ from reacting with the surface, which would otherwise reduce the IR transmission of the BCB.

Once the BCB layer is applied, the lithographic process discussed above was repeated for the second layer. The AquaSave conductive polymer was used as the conductive layer and a ring of Ni was sputtered onto the outer edge to allow for the surface height determination, while preserving the advantage of through-layer alignment.

V. SUMMARY

We explored fabrication-process options for use of BaF₂ when used in direct-write electron-beam lithography. This material is of interest for IR FSS applications because of its low refractive index and high IR transparency. Challenges in the use of this material include its low conductivity, low reflectivity, attack by solvents, and sensitivity to thermal shock. Single- and double-layer submicrometer FSS structures were demonstrated on a BaF₂ substrate.

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